

## **Victor Ovchinnikov**

Victor Ovchinnikov graduated from the Vladimir State University (Russia) in 1979 with degree in electrical engineering. He has been dealing with adaptive systems and computer technique from the very beginning of his scientific work. In 1987 he obtained his Ph.D. degree in microelectronics from the Moscow Aviation Institute. After he returned to Vladimir State University he was employed as Associate Professor. He taught courses in design and fabrication of microelectronics.



From 1997 until present day he has been serving as a Senior Research Scientist at the Microelectronics Centre, Helsinki University of Technology. He has successfully led Finnish Academia funded research projects. He is an author of 60 scientific papers published in journals and conference proceedings and 5 USSR patents.

He regularly participates in specialized scientific conferences (LDSD, ICQNM, NSM). He has been invited as co-chair and member of the Technical Committee of several Conferences organized by IARIA.

His research interests focus on finding the efficient methods of microfabrication and include low dimensional silicon structures (pillars, nanoclusters), high-impedance surfaces for millimeter wave application, ion-beam mixing for self-assembling of nanostructures and plasmonics nanostructures for metamaterial and SERS applications.